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-	883	(326/86.ccls.) and @ad<19991229		2004/08/30 16:10
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-	184	365/189.05.ccls.	US-PGPUB	2004/08/31 07:32
-	338	710/305.ccls.	US-PGPUB	2004/08/31 07:37

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PLUS Search Results for S/N 09752615, Searched August 31, 2004

The Patent Linguistics Utility System (PLUS) is a USPTO automated search

system for U.S. Patents from 1971 to the present. PLUS is a query-by-example search system which produces a list of patents that are

most closely related linguistically to the application searched. This search was prepared by the staff of the Scientific and Technical Information Center, SIRA.

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Most Frequently Occurring Classifications of Patents Returned  
From A Search of 09752615 on August 31, 2004

Original Classifications

2 710/306  
2 714/49  
2 714/724

Cross-Reference Classifications

3 365/233  
2 365/189.07  
2 711/217  
2 712/22  
2 714/37  
2 714/726  
2 714/819

Combined Classifications

3 365/233  
3 710/306  
2 365/189.02  
2 365/189.07  
2 365/200  
2 710/107  
2 710/110  
2 711/157  
2 711/217  
2 712/22  
2 714/37  
2 714/49  
2 714/724  
2 714/726  
2 714/819

09752615\_CLSTITLES

Titles of Most Frequently Occurring Classifications of Patents Returned

From A Search of 09752615 on August 31, 2004

3 365/233 (0 OR, 3 XR)  
Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL  
365/230.01 ADDRESSING  
365/233 .Sync/clocking

3 710/306 (2 OR, 1 XR)  
Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA  
PROCESSING SYSTEMS: INPUT/OUTPUT  
710/100 INTRASYSTEM CONNECTION (E.G., BUS AND BUS  
TRANSACTION PROCESSING)  
710/305 .Bus interface architecture  
710/306 ..Bus bridge

2 365/189.02 (1 OR, 1 XR)  
Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL  
365/189.01 READ/WRITE CIRCUIT  
365/189.02 .Multiplexing

2 365/189.07 (0 OR, 2 XR)  
Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL  
365/189.01 READ/WRITE CIRCUIT  
365/189.07 .Including signal comparison

2 365/200 (1 OR, 1 XR)  
Class 365 : STATIC INFORMATION STORAGE AND RETRIEVAL  
365/189.01 READ/WRITE CIRCUIT  
365/200 .Bad bit

2 710/107 (1 OR, 1 XR)  
Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA  
PROCESSING SYSTEMS: INPUT/OUTPUT  
710/100 INTRASYSTEM CONNECTION (E.G., BUS AND BUS  
TRANSACTION PROCESSING)  
710/107 .Bus access regulation

2 710/110 (1 OR, 1 XR)  
Class 710 : ELECTRICAL COMPUTERS AND DIGITAL DATA  
PROCESSING SYSTEMS: INPUT/OUTPUT  
710/100 INTRASYSTEM CONNECTION (E.G., BUS AND BUS  
TRANSACTION PROCESSING)  
710/107 .Bus access regulation  
710/110 ..Bus master/slave controlling

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2 711/157 (1 OR, 1 XR)  
 Class 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING  
 SYSTEMS: MEMORY  
 711/100 STORAGE ACCESSING AND CONTROL  
 711/154 .Control technique  
 711/157 ..Interleaving

2 711/217 (0 OR, 2 XR)  
 Class 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING  
 SYSTEMS: MEMORY  
 711/200 ADDRESS FORMATION  
 711/217 .Generating a particular pattern/sequence of  
 addresses

2 712/22 (0 OR, 2 XR)  
 Class 712 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING  
 SYSTEMS: PROCESSING ARCHITECTURES AND INS  
 TRUCTION  
 . PROCESSING  
 712/1 PROCESSING ARCHITECTURE  
 712/10 .Array processor  
 712/16 ..Array processor operation  
 712/22 ...Single instruction, multiple data (SIMD)

2 714/37 (0 OR, 2 XR)  
 Class 714 : ERROR DETECTION/CORRECTION AND FAULT  
 DETECTION/RECOVERY  
 714/100 DATA PROCESSING SYSTEM ERROR OR FAULT HANDLING  
 714/1 .Reliability and availability  
 714/25 ..Fault locating (i.e., diagnosis or testing)  
 714/37 ...Analysis (e.g., of output, state, or design  
 )

2 714/49 (2 OR, 0 XR)  
 Class 714 : ERROR DETECTION/CORRECTION AND FAULT  
 DETECTION/RECOVERY  
 714/100 DATA PROCESSING SYSTEM ERROR OR FAULT HANDLING  
 714/1 .Reliability and availability  
 714/48 ..Error detection or notification  
 714/49 ...State error (i.e., content of instruction,  
 data, or message)

2 714/724 (2 OR, 0 XR)  
 Class 714 : ERROR DETECTION/CORRECTION AND FAULT

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DETECTION/RECOVERY  
714/699 PULSE OR DATA ERROR HANDLING  
714/724 .Digital logic testing

2 714/726 (0 OR, 2 XR)  
Class 714 : ERROR DETECTION/CORRECTION AND FAULT  
DETECTION/RECOVERY  
714/699 PULSE OR DATA ERROR HANDLING  
714/724 .Digital logic testing  
714/726 ..Scan path testing (e.g., level sensitive sca  
n  
design (LSSD))

2 714/819 (0 OR, 2 XR)  
Class 714 : ERROR DETECTION/CORRECTION AND FAULT  
DETECTION/RECOVERY  
714/699 PULSE OR DATA ERROR HANDLING  
714/799 .Error/fault detection technique  
714/819 ..Comparison of data